Load cell Amplifier Unit, **LAU 73.1C.v.1.40.** (Current output) FINAL DATA, 1st. issue.

Property	Specification	Access by	Notes
Liniarity	<0.010 (one part in 10 000)		% of Full Scale
Load cell Excitation voltage	10 VDC		
Load cell Excitation system	4 wire, pins for sense wires available		Balanced or unbalanced
Load cell Drive capability	n paralleled Load cells, total: >130Ω		Ohm bridge impedance.
Input range capability and resolution	-2mV up to +23mV input; Noise< ≈0,2μV	(1/100000)	0-Max Load cell output.
Zero Offset, fixed binary steps	Range +15mV; 1mV increments	Four DIP-switches	Input at 0(or 4)mA output.
Option: Zero Offset, fine trim	-1,5mV or 0,07mV/360° turn	20 turn 3/8' pot.	Only if UA board apply
Relative gain, fixed binary steps	Range 1-8*; increments of 1*	Three DIP-switches	1*: 20mV _{inp} at 20mA _{out} 8*: 2.5mV _{inp} at 20mA _{out}
Option: Sensitivity, fine trim	-1,2* (non-linear)	20 turn 3/8' pot.	Only if UA board apply
Signal filter, active, low pass	33; 3.3 or 0.33Hz	Two DIP-switches	
Current Output	4-20 mA or 0-20 mA	One DIP-switch	Reverse current protected
Option: Voltage Output	10VDC at 500Ω load	1k//1kΩ resistors	Provided by the UA board.
Temperature Effect on Zero	Zero<50ppm/°C at 0mV input; <100ppm/°C at 10mV input. Given no		Given no fine trim pots
Temperature Effect on Gain	Gain<50ppm/°C		Given no fine trim pots
Temperature Range	Compensated -10°C/+40°C; Operating -20°C/+50°C		
Power Supply	12-14VDC 50-90mA Regulated	Not isolated	Over voltage and reversed polarity and ESD protected.
Housing and connections	PCB and all metal shield case. Input and output: Rows of lugs or pins: 2,54mm pitch		
Dimensions, weight and mounting	L81.3 * W30.5 * H6,1 mm; Weighing 26 gram; Bolt mounting 2*ø3,2mm holes in one end.		
Environmental protection	IP40;		
Conform to Council Directives	CE in accordance with 73/23/EEC; 93/98/EEC and 89/336/EEC		